

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re U.S. Patent Application**

**SAOTOME et al.**

**Application Number: To be assigned**

**Filed: Concurrently herewith**

**For: SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE**

**Attorney Docket No. HITA.0521**

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**Art Unit: 2826**

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**INFORMATION DISCLOSURE STATEMENT**

The above-referenced application is a Divisional of U.S. Serial No. 10/177,044 filed June 24, 2002. It includes the same disclosure as U.S. patent application Serial No. 10/177,044.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application, pursuant to 37 CFR § 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

This Information Disclosure Statement is submitted with the above-captioned U.S. Divisional application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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**March 4, 2004**

Form PTO 1449  U.S. Department of Commerce Patent and Trademark Office  Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER <b>HITA.0521</b>	SERIAL NUMBER <b>To Be Assigned</b>
	APPLICANT <b>Saotome et al.</b>	
	FILING DATE <b>Concurrently Herewith</b>	GROUP

## U.S. Patent Documents

Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	*	5,898,636	04/27/1999	Isomura et al.			06/07/1994
	*	6,034,912	03/07/2000	Isomura et al.			09/01/1998
	*	5,790,839	08/04/1998	Luk et al.			12/20/1996
	**	5,323,033	06/21/1994	Matsumoto et al.			08/04/1992
	**	6,191,990	02/20/2001	Itoh et al.			02/22/2000
	**	5,208,782	05/04/1993	Sakuta et al.			05/29/1992
	**	5,091,883	02/25/1992	Matsuzaki et al.			07/13/1990

## Foreign Patent Documents

Examiner Initial		DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
							YES	NO
	*	7-78874	06/21/1993	Japan				X
	*	10-189889	12/01/1997	Japan				X
	**	JP-03116868-A	09/29/1989	Japan			Abstract	

## Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)


EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant*

PTO1449

\* Filed by Applicant in Parent

\*\* Cited by Examiner in Parent